

10/099641
03/15/02

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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10099641	03/15/2002	438	757	2829	
**APPLICANTS: Catabay Wilbur; Schinella Richard;					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A DIV OF 09/426,056 10/22/1999 PAT 6,391,795 <i>WAC Schinella Wilbur</i>					
** FOREIGN APPLICATIONS VERIFIED: <i>WAC none 12/14/02</i>					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO	
Verified and Acknowledged Examiners's Initials <i>WAC</i>				99-102/1D	
TITLE : Low k dielectric composite layer for integrated circuit structure which provides void-free low k dielectric material between metal lines while mitigating via poisoning <small>U.S. DEPT. OF COMM. PAT. & TM. PTO-436L (Rev. 12-94)</small>					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	Total Claims <input type="checkbox"/> Print Claims for O.G. <input type="checkbox"/>
ISSUE FEE		Primary Examiner	DRAWING
Amount Due	Date Paid		Sheets Drawn <input type="checkbox"/> Figs. Drawn <input type="checkbox"/> Print Fig. <input type="checkbox"/>
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner
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